1763

#### SUBMISSION OF FORMAL DRAWINGS Serial No. 10/055,310



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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#### PATENT APPLICATION

Applicant: Noorbakhsh, et al.

Case: 4150D1/ETCH/DRIE/JB1

Serial No.: 10/055,310

Filed: January 22, 2002

Examiner: Luz L. Alejandro Mulero

Group Art Unit: 1763

Title:

TEMPERATURE CONTROLLED SEMICONDUCTOR PROCESSING

**CHAMBER LINER** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

### **SUBMISSION OF FORMAL DRAWINGS**

The Applicants submit herewith <u>9</u> sheets of replacement formal drawings (FIGS. 1 through 67F) in connection with the above-captioned application.

Respectfully submitted,

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## CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited on <u>July 22</u>, <u>2003</u> with the United States Postal Service as first-class mail, with sufficient postage, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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